09894160 .100101

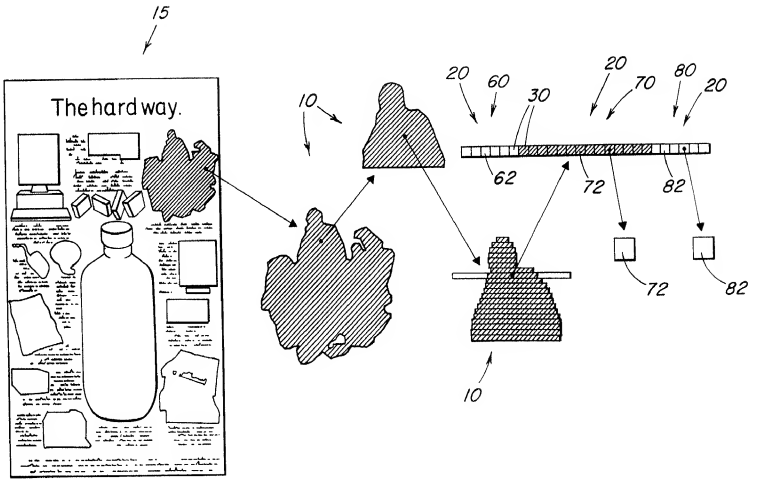

FIG. 1

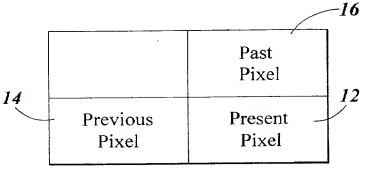

FIG. 2

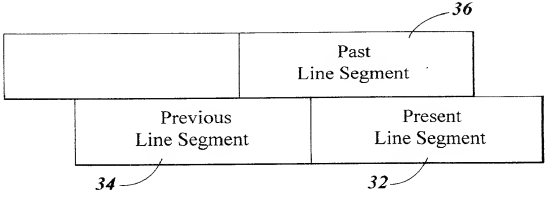

FIG. 3

09941150 - 100101

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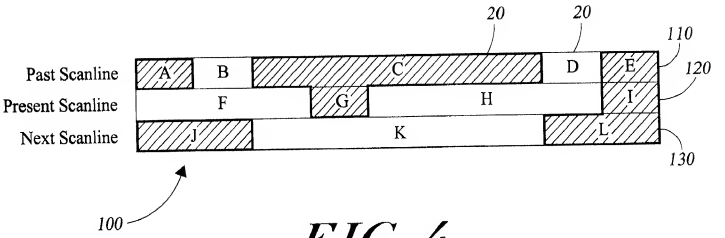

FIG. 4

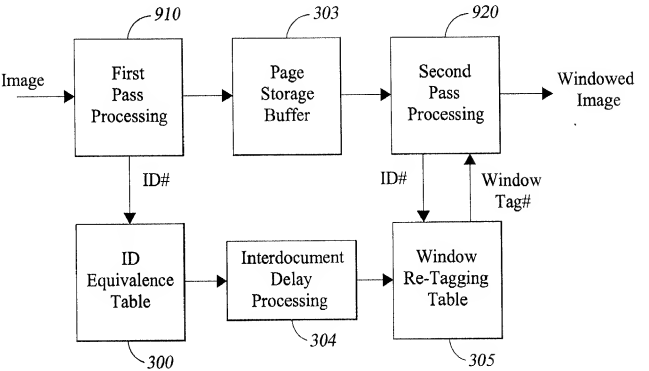
910
303
920
Image
First Pass Processing
Page Storage Buffer
Second Pass Processing
Windowed Image
ID#
ID#
Window Tag#
ID Equivalence Table
Interdocument Delay Processing
Window Re-Tagging Table
300
304
305

FIG. 5

TOTOOT"09Th6860

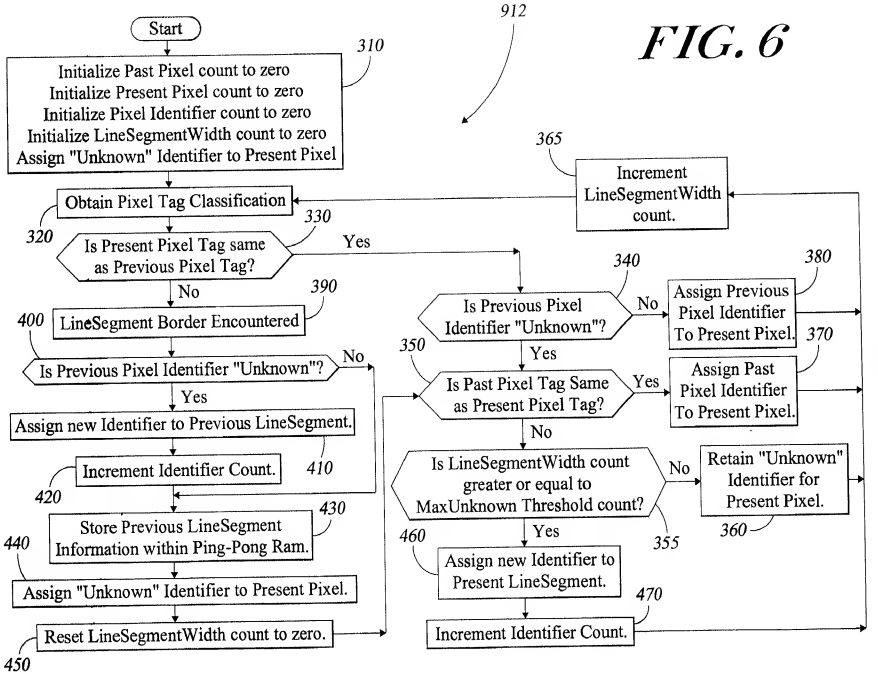

FIG. 6

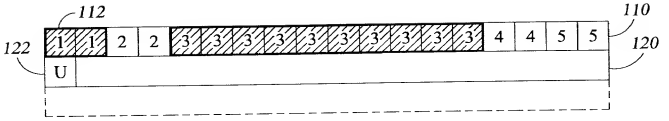

FIG. 7A

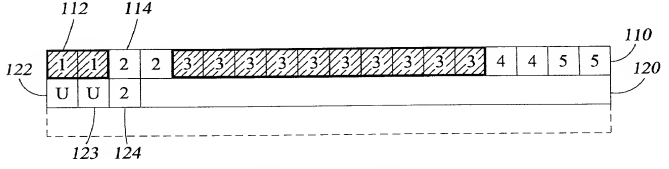

FIG. 7B

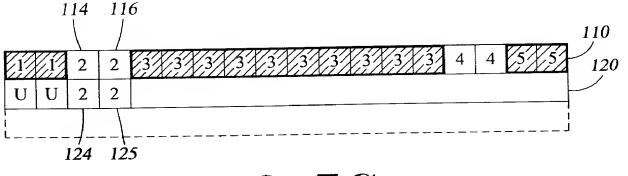

FIG. 7C

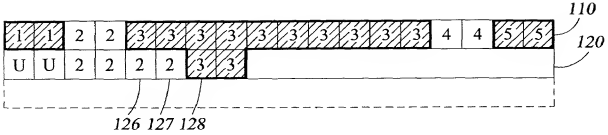

FIG. 7D

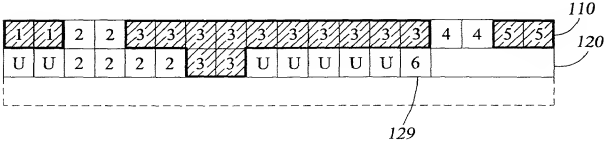

FIG. 7E

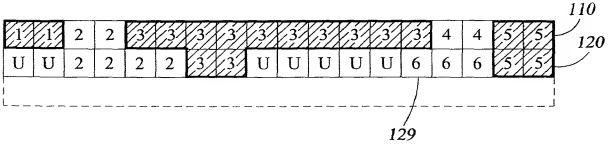

FIG. 7F

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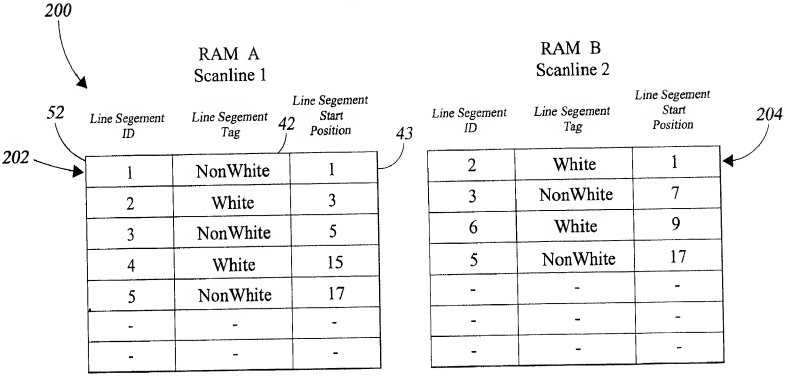
FIG. 8



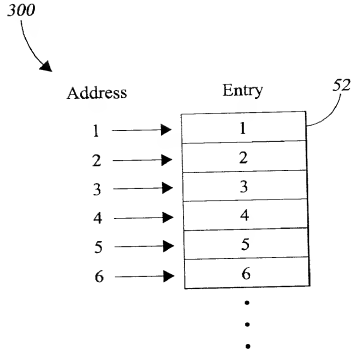

300
Address
Entry
52
1
2
3
4
5
6
1
2
3
4
5
6

FIG. 9

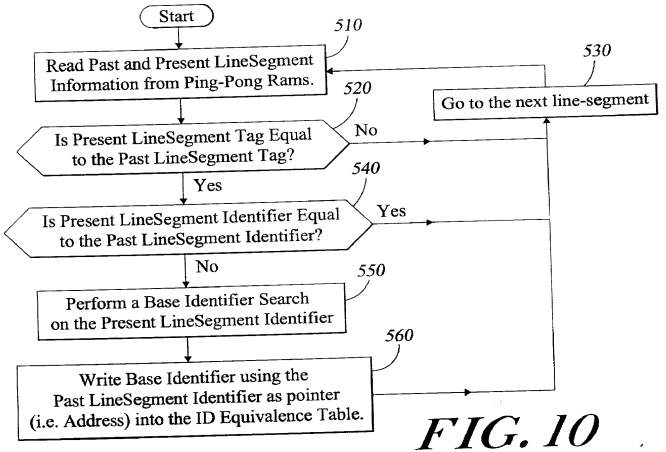

Start
510
Read Past and Present LineSegment Information from Ping-Pong Rams.
520
Is Present LineSegment Tag Equal to the Past LineSegment Tag?
No
Yes
540
Is Present LineSegment Identifier Equal to the Past LineSegment Identifier?
Yes
No
550
Perform a Base Identifier Search on the Present LineSegment Identifier
560
Write Base Identifier using the Past LineSegment Identifier as pointer (i.e. Address) into the ID Equivalence Table.
530
Go to the next line-segment

FIG. 10

09894160 "100102

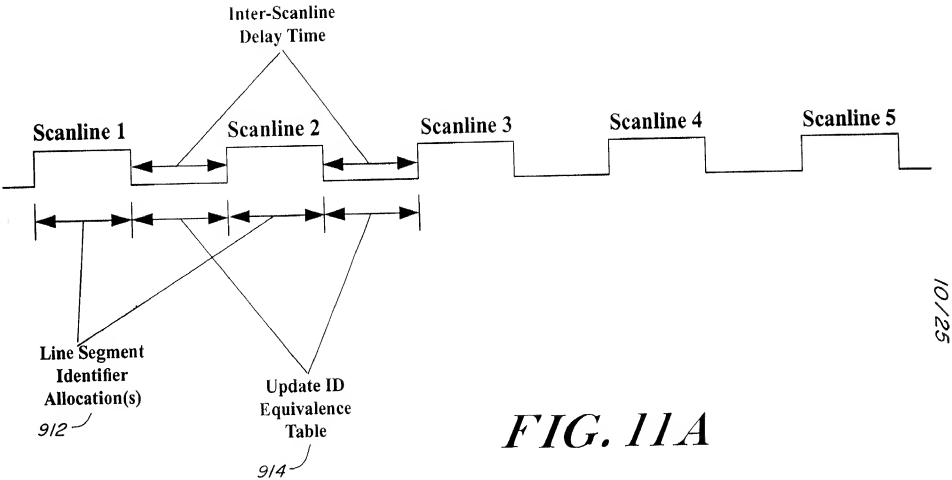
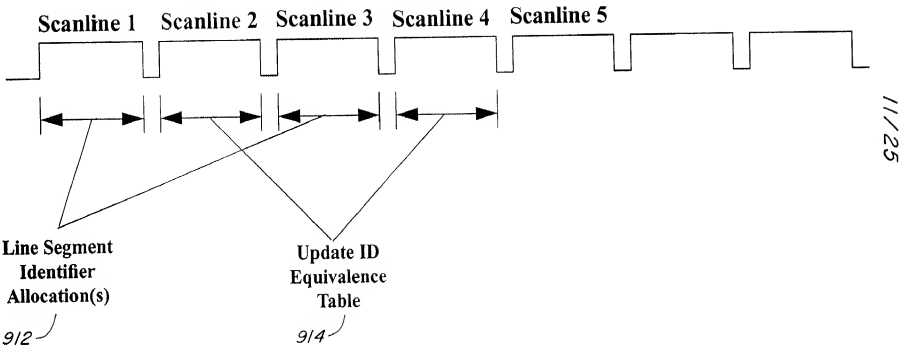


FIG. 11A

TOTOOT*09Th6860

FIG. 11B



09894160 - 100103

Address	Entry
1	1
2	2
3	3
4	6
5	5
6	6
7	7
-	-

FIG. 12

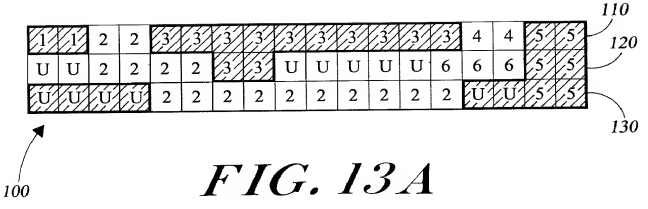


FIG. 13A

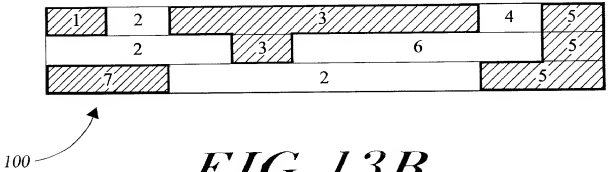


FIG. 13B

TOTOOT*09Th6860

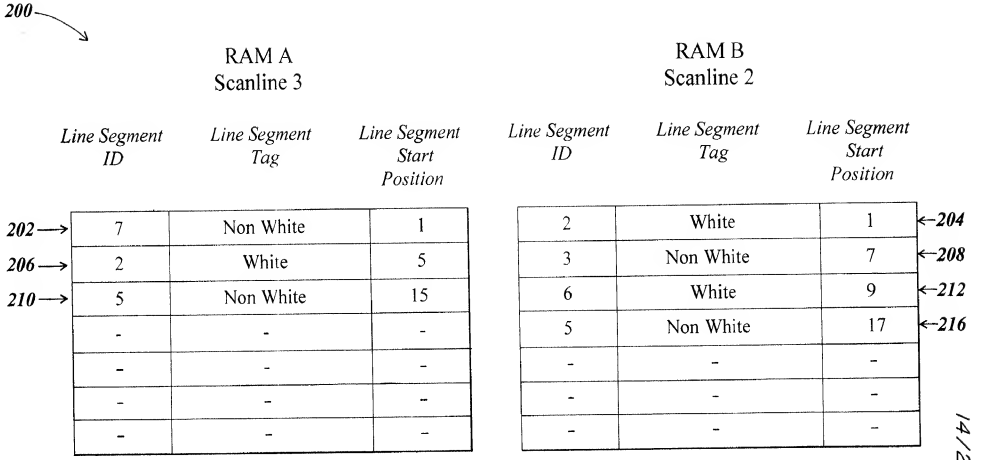


FIG. 14

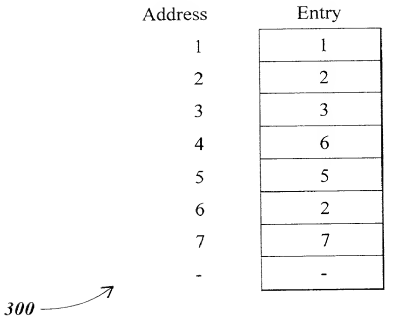


FIG. 15

10/007,091 Filed 11/09/2001

TOTOOT "09Th6860

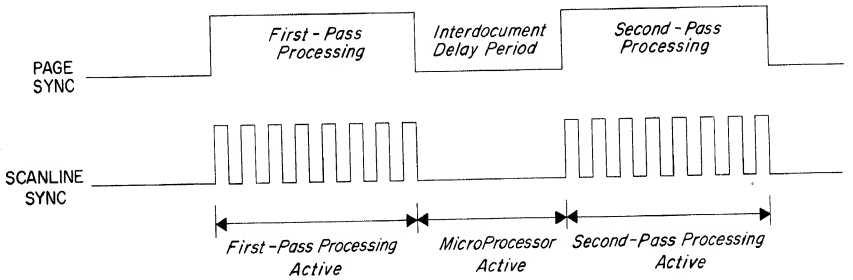


FIG. 16

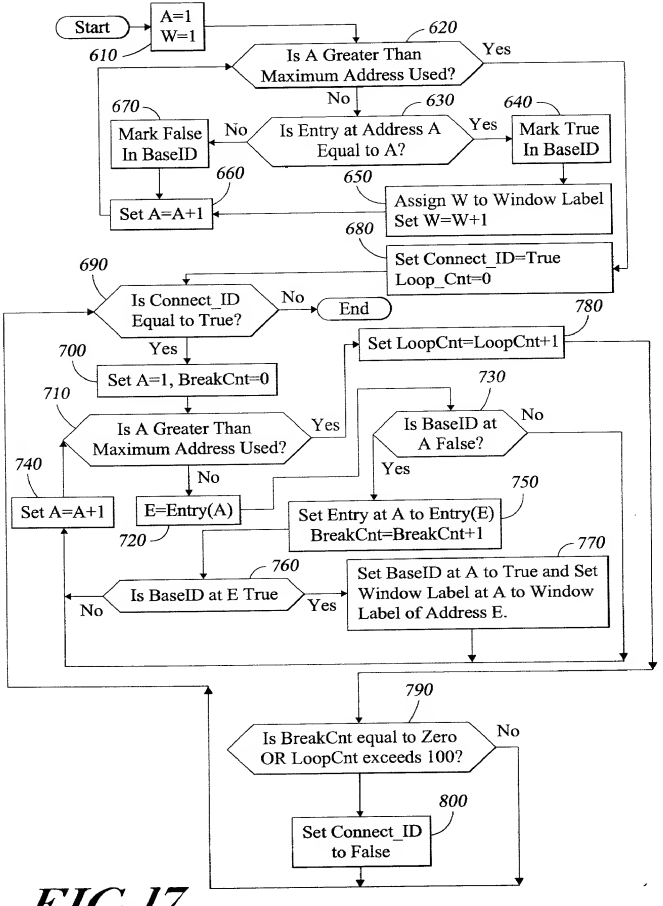


FIG.17

US 6,988,151 B2

0589-150-10010

302 →

Address	307 Entry	308 BaseID	309 Window
1	1	True	W1
2	2	True	W2
3	3	True	W3
4	6	False	
5	5	True	W4
6	2	False	
7	7	True	W5
-	-	-	-

FIG. 18A

302 →

Address	307 Entry	308 BaseID	309 Window
1	1	True	W1
2	2	True	W2
3	3	True	W3
4	2	False	
5	5	True	W4
6	2	True	W2
7	7	True	W5
-	-	-	-

FIG. 18B

302

Address	Entry (307)	BaseID (308)	Window (309)
1	1	True	W1
2	2	True	W2
3	3	True	W3
4	2	True	W2
5	5	True	W4
6	2	True	W2
7	7	True	W5
-	-	-	-

FIG. 18C

US 2008/0151901 A1

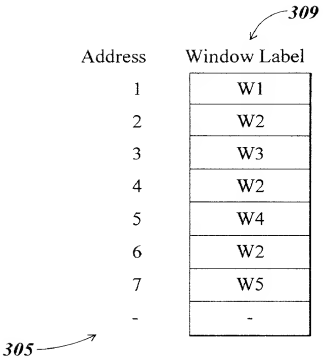


FIG. 19

09541502.100101

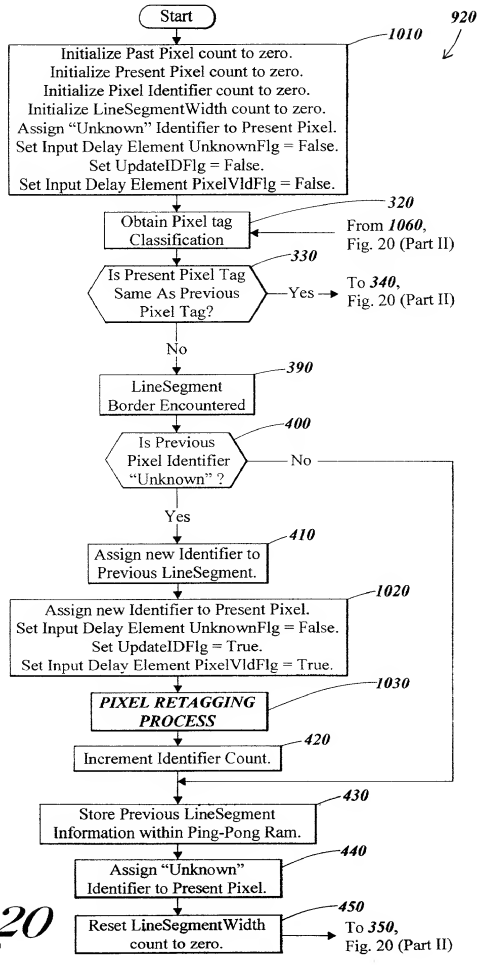
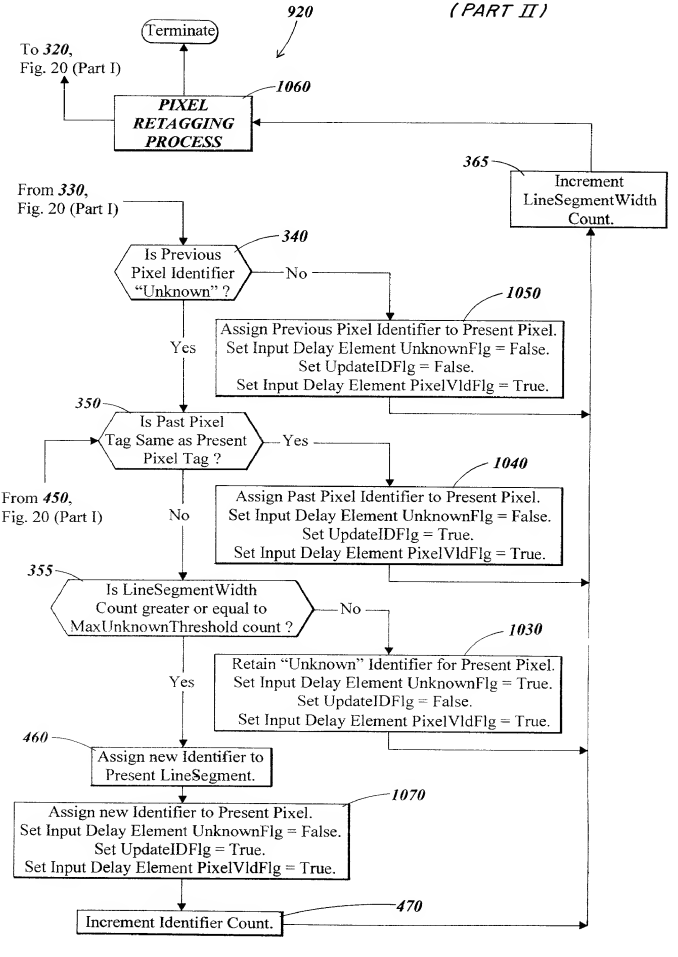


FIG. 20
(PART I)

0989160 - 100101

FIG. 20
(PART II)



10/001,051

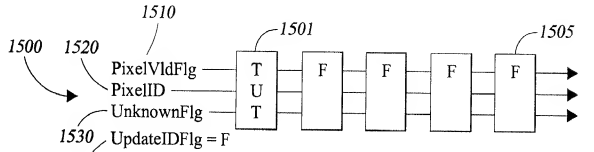


FIG. 21A

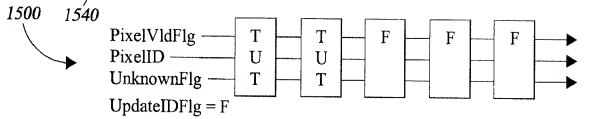


FIG. 21B

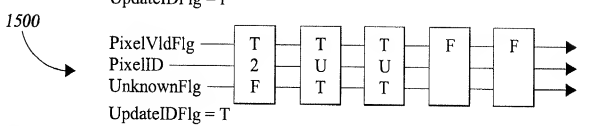


FIG. 21C

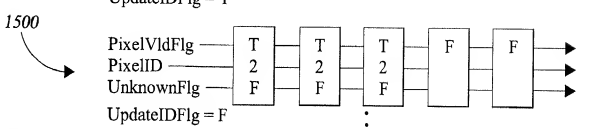


FIG. 21D

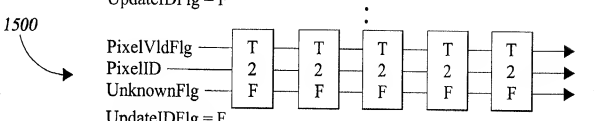


FIG. 21E

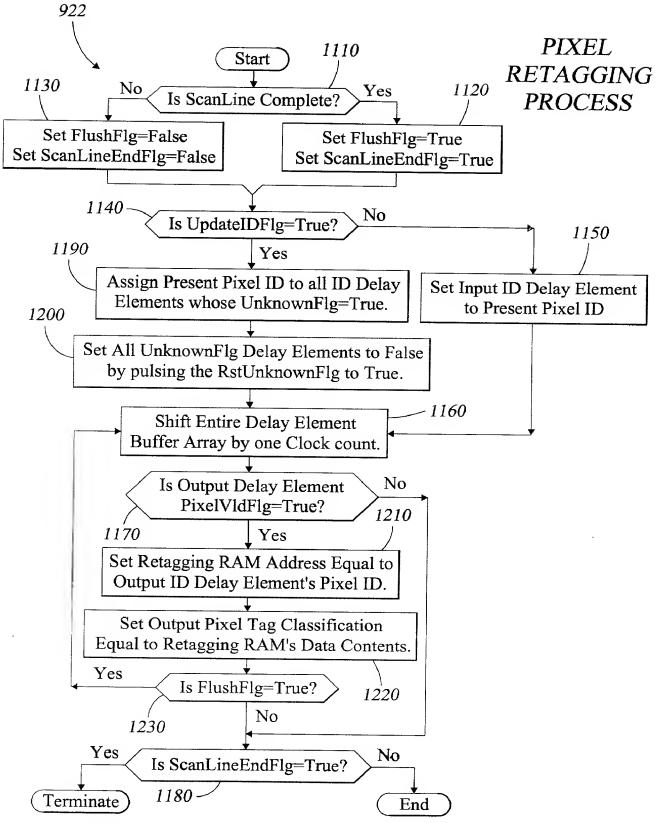


FIG. 22

09804150 - 100101

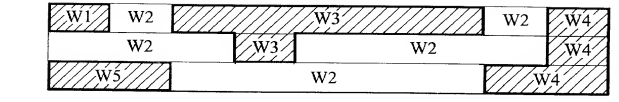

W1
W2
W3
W2
W4
W2
W3
W2
W4
W5
W2
W4
100

FIG. 23

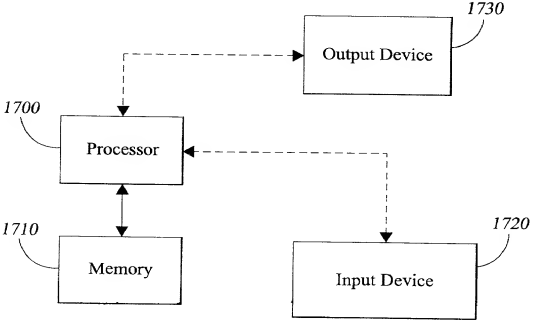

1730
Output Device
1700
Processor
1710
Memory
1720
Input Device

FIG. 24